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SPECIFICATIONS FOR LCD MODULE

| | |
|-------------------|------------|
| CUSTOMER | |
| CUSTOMER PART NO. | |
| ACMMI PART NO. | AMG240128B |
| DESCRIPTION | |
| APPROVED BY | |
| DATE | |

| PREPARED BY | CHECKED BY | APPROVED BY |
|-------------|------------|-------------|
| | | |

DOCUMENT REVISION HISTORY:

| DATE | PAGE | DESCRIPTION |
|------------|--------|---------------|
| 2006-10-7. | - - | First release |

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1. Module Classification Information

A M C 1 6 0 2 A R - B - B 6 W T D W - S P
 1 2 3 4 5 6 7 8 9 10 11 12 13

| | |
|----|---|
| 1 | Brand : Orient Display (N.A.) Ltd. |
| 2 | Display Type : C→ Character Type, G→ Graphic Type, NONE→ Custom-made |
| 3 | Display Font : Characters X Lines / Rows X Columns /Others |
| 4 | Model serials no. |
| 5 | RoHS compliant: R→YES NONE→ NO |
| 6 | IC Package Type: M→ SMT Type B→ COB Type T→ TAB Type G→ COG Type F→ COF Type S→ Special |
| 7 | LCD Mode: P→TN Positive N→TN Negative Y→ STN Positive, Yellow Green B→ STN Negative, Blue G→ STN Positive, Gray W→ FSTN Positive T→ FSTN Negative F→ FFSTN Negative S→ Special |
| 8 | Viewing direction 6→ 6:00,12→12:00, S→Special |
| 9 | Temperature range N → Normal Temperature W→ Wide Temperature S→ Special |
| 10 | LCD Polarizer Type R→ Reflective T→ Transmissive F→ Transflective S→ Special |
| 11 | Backlight Type N→ None D→ LED E→ EL F→ CCFL S→ Special |
| 12 | Backlight Color Y→ Yellow-green B→ Blue A→ Amber W→ White G→ Green R→ Red S→ Special |
| 13 | Internal Code |

2. Precautions in use of LCD Modules

- (1) Avoid applying excessive shocks to the module or making any alterations or modifications to it.
- (2) Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.
- (3) Don't disassemble the LCM.
- (4) Don't operate it above the absolute maximum rating.
- (5) Don't drop, bend or twist LCM.
- (6) Soldering: only to the I/O terminals.
- (7) Storage: please storage in anti-static electricity container and clean environment.

3. General Specification

| Item | Dimension | Unit |
|-----------------------------------|------------------------------------|-------------|
| Number of Dots | 240 x 128 | — |
| Module dimension(None Backlight) | 144.0 x 104.0 x 13.0 (MAX) | mm |
| Module dimension(With Backlight) | 144.0 x 104.0 x 15.0 (MAX) | mm |
| View area | 114.0 x 64.0 | mm |
| Active area | 107.95 x 57.55 | mm |
| Dot size | 0.40 x 0.40 | mm |
| Dot pitch | 0.45x 0.45 | mm |
| LCD type | STN | |
| Duty | 1/128 | |
| View direction | 6 o'clock or 12 o'clock | |
| Backlight Type | None, YELLOW-GREEN,WHITE backlight | |

4. Absolute Maximum Ratings

| Item | | Symbol | Min | Max | Unit |
|-----------------------------|-----------------|---------------------------------|----------|---------|------|
| Input Voltage | | V_I | -0.3 | VDD+0.3 | V |
| Supply Voltage For Logic | | VDD-V _{SS} | -0.3 | 7.0 | V |
| Supply Voltage For LCD | | V _{DD} -V ₀ | Vdd-13.5 | 0 | V |
| Standard Temperature LCM | Operating Temp. | Top | 0 | 50 | °C |
| | Storage Temp. | Tstr | -10 | 60 | °C |
| Wide Temperature LCM | Operating Temp. | Top | -20 | 70 | °C |
| | Storage Temp. | Tstr | -30 | 80 | °C |

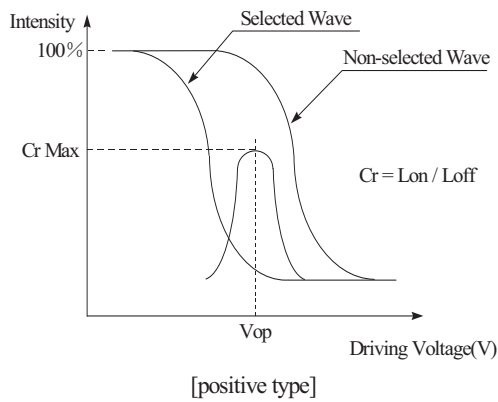
5. Electrical Characteristics

| Item | Symbol | Condition | Min | Typ | Max | Unit |
|---|----------------------------------|--|---------------------|------|---------------------|------|
| Supply Voltage For Logic | V _{DD} -V _{SS} | — | 4.5 | 5.0 | 5.5 | V |
| Supply Voltage For LCD | V _{DD} -V ₀ | Ta=25°C | 18.0 | 18.5 | 19.0 | V |
| Input High Volt. | V _{IH} | — | 0.7 V _{DD} | — | V _{DD} | V |
| Input Low Volt. | V _{IL} | — | V _{SS} | — | 0.3 V _{DD} | V |
| Supply Current | I _{DD} | V _{DD} =5V | 8.5 | 9.5 | 12.5 | mA |
| Supply Voltage of Yellow-green backlight | V _{LED} | Forward current =720 mA Number of LED die 2x72= 144 | 3.8 | 4.2 | 4.3 | V |
| Supply Voltage of White backlight | V _{LED} | Forward current =90 mA | 2.9 | 3.1 | 3.3 | V |

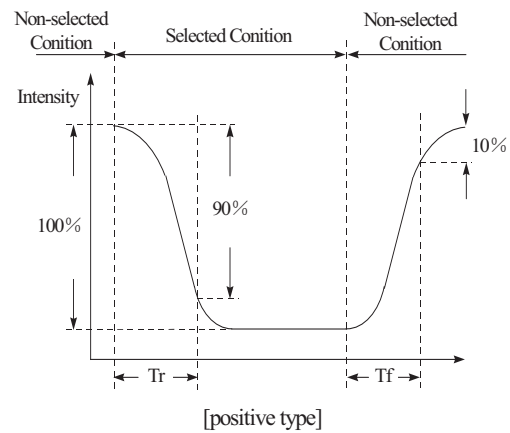
6. Optical Characteristics

| Item | Symbol | Condition | Min | Typ | Max | Unit |
|----------------|---------------|-------------|-----|-----|-----|------|
| View Angle | (V) θ | $CR \geq 2$ | -20 | — | 35 | deg |
| | (H) φ | $CR \geq 2$ | -30 | — | 30 | deg |
| Contrast Ratio | CR | — | — | 3 | — | — |
| Response Time | T rise | — | — | — | 250 | ms |
| | T fall | — | — | — | 250 | ms |

Definition of Operation Voltage (Vop)



Definition of Response Time (Tr, Tf)



Conditions :

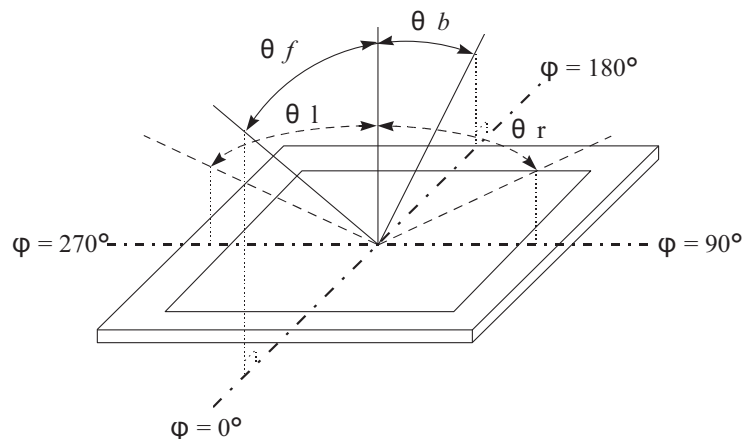
Operating Voltage : Vop

Viewing Angle(θ , φ) : 0° , 0°

Frame Frequency : 64 HZ

Driving Waveform : 1/N duty , 1/a bias

Definition of viewing angle($CR \geq 2$)

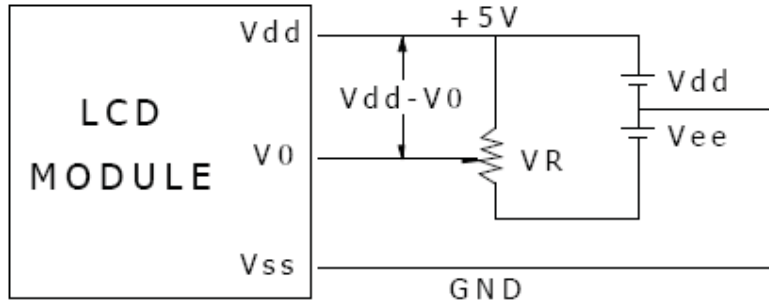


7. Interface Pin Function

| Pin No. | Symbol | Level | Description |
|----------------|-----------------|--------------|--------------------------------------|
| 1 | FGND | | Frame GND |
| 2 | V _{SS} | 0V | Ground |
| 3 | V _{DD} | 5.0V | Supply Voltage for logic |
| 4 | V ₀ | | Supply voltage for LCD |
| 5 | /WR | H/L | Write Data into T6963C |
| 6 | /RD | H/L | Read Data from T6963C |
| 7 | /CS | H/L | Chip enable for T6963C |
| 8 | C/D | H/L | Data write/read |
| 9 | NC | H/L | NC |
| 10 | /RST | H/L | Reset signal |
| 11~18 | DB0~DB7 | H/L | Data bus |
| 19 | FS | H/L | Pins for selection of font |
| 20 | VEE | H/L | Data bit 3 |
| 21/33 | LED(+) | | Anode of LED Backlight |
| 22/34 | LED(-) | | Cathode of LED Backlight |
| 23 | ED | | Data output for columns |
| 24 | CDATA | | Synchronization signal for row drive |
| 25 | M | | Frame signal |
| 26 | LP | | Latch pulse and shift clock pulse |
| 27 | HSCP | | Shift clock pulse for columns drive |
| 28 | NC | | No connector |
| 29 | VDD | | Supply voltage for LCD(+) |
| 30 | VSS | | Ground |
| 31 | VO | | Supply voltage for LCD(-) |
| 32 | VEE | | Negative voltage output |

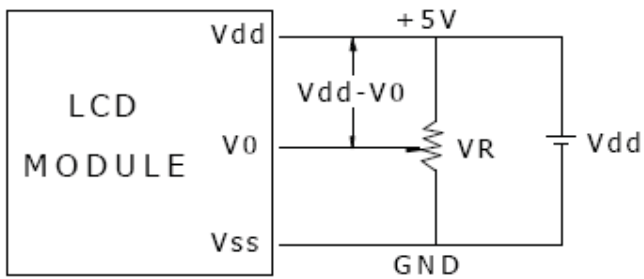
8. POWER SUPPLY

Without Negative Power on PCB



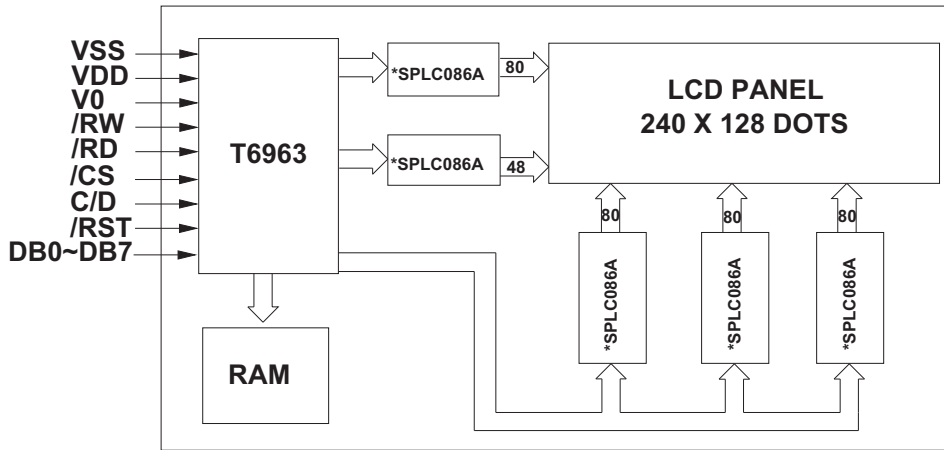
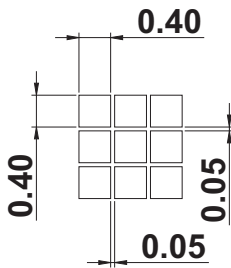
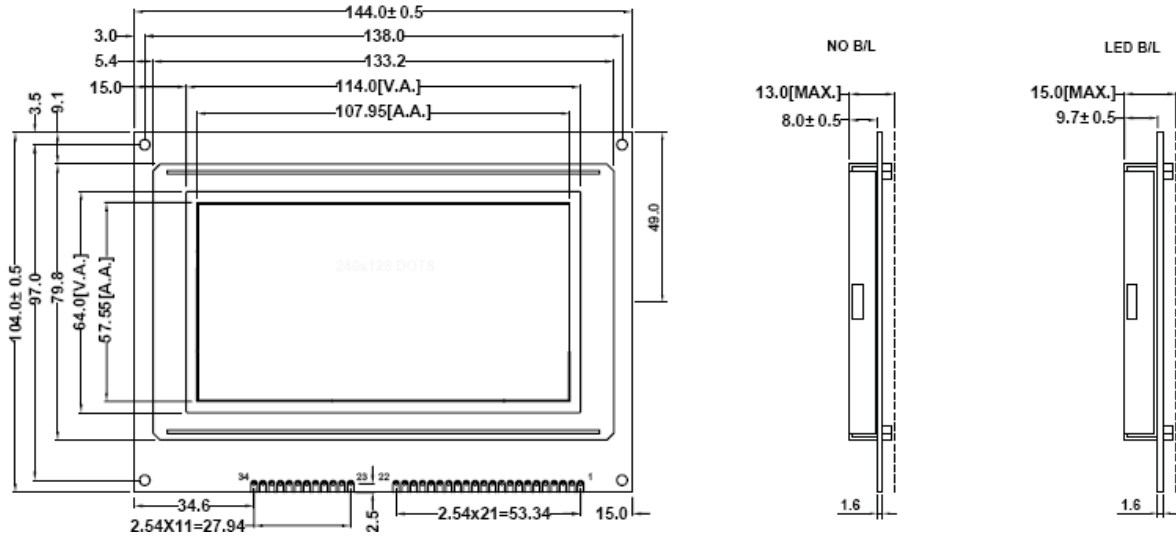
Vdd-V0: LCD Driving Voltage
VR: 10K - 20K

With Negative Power on PCB



Vdd-V0: LCD Driving Voltage
VR: 10K - 20K

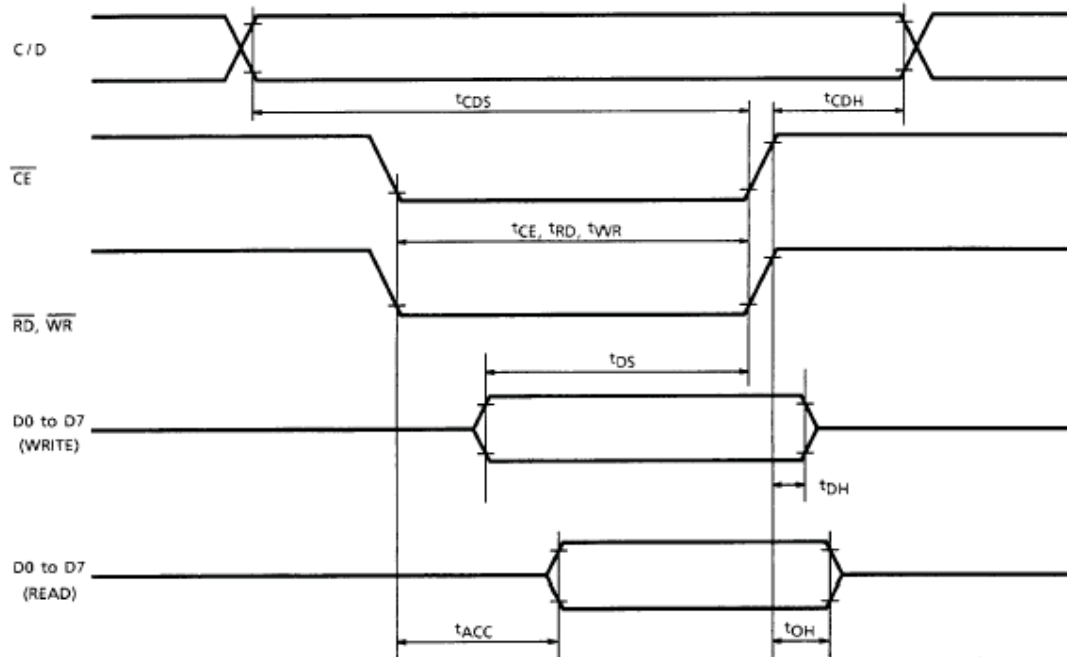
9. Contour Drawing & Block Diagram



** OR EQUIVALENT

10. Timing Characteristics

Bus Timing



TEST CONDITIONS (Unless otherwise noted, $V_{DD} = 5.0V \pm 10\%$, $V_{SS} = 0V$, $T_a = -20$ to $75^\circ C$)

| ITEM | SYMBOL | TEST CONDITIONS | MIN | MAX | UNIT |
|--------------------------------------|--------------------------|-----------------|-----|-----|------|
| C/D Set-up Time | t_{CDS} | — | 100 | — | ns |
| C/D Hold Time | t_{CDH} | — | 10 | — | ns |
| \overline{CE} , RD, WR Pulse Width | t_{CE}, t_{RD}, t_{WR} | — | 80 | — | ns |
| Data Set-up Time | t_{DS} | — | 80 | — | ns |
| Data Hold Time | t_{DH} | — | 40 | — | ns |
| Access Time | t_{ACC} | — | — | 150 | ns |
| Output Hold Time | t_{OH} | — | 10 | 50 | ns |

11. Table of T6963C Commands

COMMAND DEFINITIONS

| COMMAND | CODE | D1 | D2 | FUNCTION |
|------------------------|----------|-------------|--------------|--------------------------------|
| REGISTERS SETTING | 00100001 | X address | Y address | Set Cursor Pointer |
| | 00100010 | Data | 00H | Set Offset Register |
| | 00100100 | Low address | High address | Set Address Pointer |
| SET CONTROL WORD | 01000000 | Low address | High address | Set Text Home Address |
| | 01000001 | Columns | 00H | Set Text Area |
| | 01000010 | Low address | High address | Set Graphic Home Address |
| | 01000011 | Columns | 00H | Set Graphic Area |
| MODE SET | 1000X000 | — | — | OR mode |
| | 1000X001 | — | — | EXOR mode |
| | 1000X011 | — | — | AND mode |
| | 1000X100 | — | — | Text Attribute mode |
| | 10000XXX | — | — | Internal CG ROM mode |
| | 10001XXX | — | — | External CG RAM mode |
| DISPLAY MODE | 10010000 | — | — | Display off |
| | 1001XX10 | — | — | Cursor on, blink off |
| | 1001XX11 | — | — | Cursor on, blink on |
| | 100101XX | — | — | Text on, graphic off |
| | 100110XX | — | — | Text off, graphic on |
| | 100111XX | — | — | Text on, graphic on |
| CURSOR PATTERN SELECT | 10100000 | — | — | 1-line cursor |
| | 10100001 | — | — | 2-line cursor |
| | 10100010 | — | — | 3-line cursor |
| | 10100011 | — | — | 4-line cursor |
| | 10100100 | — | — | 5-line cursor |
| | 10100101 | — | — | 6-line cursor |
| | 10100110 | — | — | 7-line cursor |
| | 10100111 | — | — | 8-line cursor |
| DATA AUTO READ / WRITE | 10110000 | — | — | Set Data Auto Write |
| | 10110001 | — | — | Set Data Auto Read |
| | 10110010 | — | — | Auto Reset |
| DATA READ / WRITE | 11000000 | Data | — | Data Write and Increment ADP |
| | 11000001 | — | — | Data Read and Increment ADP |
| | 11000010 | Data | — | Data Write and Decrement ADP |
| | 11000011 | — | — | Data Read and Decrement ADP |
| | 11000100 | Data | — | Data Write and Nonvariable ADP |
| | 11000101 | — | — | Data Read and Nonvariable ADP |
| SCREEN PEEK | 11100000 | — | — | Screen Peek |
| SCREEN COPY | 11101000 | — | — | Screen Copy |

X : invalid

| COMMAND | CODE | D1 | D2 | FUNCTION |
|-----------------|----------|----|----|-------------|
| BIT SET / RESET | 11110XXX | — | — | Bit Reset |
| | 11111XXX | — | — | Bit Set |
| | 1111X000 | — | — | Bit 0 (LSB) |
| | 1111X001 | — | — | Bit 1 |
| | 1111X010 | — | — | Bit 2 |
| | 1111X011 | — | — | Bit 3 |
| | 1111X100 | — | — | Bit 4 |
| | 1111X101 | — | — | Bit 5 |
| | 1111X110 | — | — | Bit 6 |
| | 1111X111 | — | — | Bit 7 (MSB) |

X : invalid

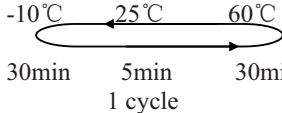
12. Quality Assurance

Screen Cosmetic Criteria

| Item | Defect | Judgment Criterion | Partition | | | | | | | | | | | | | | | | | | | | |
|--------------------|--------------------------------------|--|-------------------|--------------------------------------|--------------|-----------|--------------------|---|--------------------|---|-----------|---|-------------------|--------------------------------------|--------------|-----------|--------------------|---|--------------------|---|-----------|---|-------|
| 1 | Spots | <p>A) Clear</p> <table border="0"> <tr> <td><u>Size: d mm</u></td> <td><u>Acceptable Qty in active area</u></td> </tr> <tr> <td>$d \leq 0.1$</td> <td>Disregard</td> </tr> <tr> <td>$0.1 < d \leq 0.2$</td> <td>6</td> </tr> <tr> <td>$0.2 < d \leq 0.3$</td> <td>2</td> </tr> <tr> <td>$0.3 < d$</td> <td>0</td> </tr> </table> <p>Note: Including pin holes and defective dots which must be within one pixel size.</p> <p>B) Unclear</p> <table border="0"> <tr> <td><u>Size: d mm</u></td> <td><u>Acceptable Qty in active area</u></td> </tr> <tr> <td>$d \leq 0.2$</td> <td>Disregard</td> </tr> <tr> <td>$0.2 < d \leq 0.5$</td> <td>6</td> </tr> <tr> <td>$0.5 < d \leq 0.7$</td> <td>2</td> </tr> <tr> <td>$0.7 < d$</td> <td>0</td> </tr> </table> | <u>Size: d mm</u> | <u>Acceptable Qty in active area</u> | $d \leq 0.1$ | Disregard | $0.1 < d \leq 0.2$ | 6 | $0.2 < d \leq 0.3$ | 2 | $0.3 < d$ | 0 | <u>Size: d mm</u> | <u>Acceptable Qty in active area</u> | $d \leq 0.2$ | Disregard | $0.2 < d \leq 0.5$ | 6 | $0.5 < d \leq 0.7$ | 2 | $0.7 < d$ | 0 | Minor |
| <u>Size: d mm</u> | <u>Acceptable Qty in active area</u> | | | | | | | | | | | | | | | | | | | | | | |
| $d \leq 0.1$ | Disregard | | | | | | | | | | | | | | | | | | | | | | |
| $0.1 < d \leq 0.2$ | 6 | | | | | | | | | | | | | | | | | | | | | | |
| $0.2 < d \leq 0.3$ | 2 | | | | | | | | | | | | | | | | | | | | | | |
| $0.3 < d$ | 0 | | | | | | | | | | | | | | | | | | | | | | |
| <u>Size: d mm</u> | <u>Acceptable Qty in active area</u> | | | | | | | | | | | | | | | | | | | | | | |
| $d \leq 0.2$ | Disregard | | | | | | | | | | | | | | | | | | | | | | |
| $0.2 < d \leq 0.5$ | 6 | | | | | | | | | | | | | | | | | | | | | | |
| $0.5 < d \leq 0.7$ | 2 | | | | | | | | | | | | | | | | | | | | | | |
| $0.7 < d$ | 0 | | | | | | | | | | | | | | | | | | | | | | |
| 2 | Bubbles in Polarizer | <table border="0"> <tr> <td><u>Size: d mm</u></td> <td><u>Acceptable Qty in active area</u></td> </tr> <tr> <td>$d \leq 0.3$</td> <td>Disregard</td> </tr> <tr> <td>$0.3 < d \leq 1.0$</td> <td>3</td> </tr> <tr> <td>$1.0 < d \leq 1.5$</td> <td>1</td> </tr> <tr> <td>$1.5 < d$</td> <td>0</td> </tr> </table> | <u>Size: d mm</u> | <u>Acceptable Qty in active area</u> | $d \leq 0.3$ | Disregard | $0.3 < d \leq 1.0$ | 3 | $1.0 < d \leq 1.5$ | 1 | $1.5 < d$ | 0 | Minor | | | | | | | | | | |
| <u>Size: d mm</u> | <u>Acceptable Qty in active area</u> | | | | | | | | | | | | | | | | | | | | | | |
| $d \leq 0.3$ | Disregard | | | | | | | | | | | | | | | | | | | | | | |
| $0.3 < d \leq 1.0$ | 3 | | | | | | | | | | | | | | | | | | | | | | |
| $1.0 < d \leq 1.5$ | 1 | | | | | | | | | | | | | | | | | | | | | | |
| $1.5 < d$ | 0 | | | | | | | | | | | | | | | | | | | | | | |
| 3 | Scratch | In accordance with spots cosmetic criteria. When the light reflects on the panel surface, the scratches are not to be remarkable. | Minor | | | | | | | | | | | | | | | | | | | | |
| 4 | Allowable Density | Above defects should be separated more than 30mm each other. | Minor | | | | | | | | | | | | | | | | | | | | |
| 5 | Coloration | Not to be noticeable coloration in the viewing area of the LCD panels. Back-light type should be judged with back-light on state only. | Minor | | | | | | | | | | | | | | | | | | | | |

13. Reliability

Content of Reliability Test

| Environmental Test | | | |
|--------------------------------------|--|---|---------------------|
| Test Item | Content of Test | Test Condition | Applicable Standard |
| High Temperature storage | Endurance test applying the high storage temperature for a long time. | 60°C 96hrs | — |
| Low Temperature storage | Endurance test applying the high storage temperature for a long time. | -10°C 96hrs | — |
| High Temperature Operation | Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time. | 50°C 96hrs | — |
| Low Temperature Operation | Endurance test applying the electric stress under low temperature for a long time. | 0°C 96hrs | — |
| High Temperature/ Humidity Storage | Endurance test applying the high temperature and high humidity storage for a long time. | 60°C, 90%RH 96hrs | — |
| High Temperature/ Humidity Operation | Endurance test applying the electric stress (Voltage & Current) and temperature / humidity stress to the element for a long time. | 50°C, 90%RH 96hrs | — |
| Temperature Cycle | Endurance test applying the low and high temperature cycle.  | -10°C/60°C 10 cycles | — |
| Mechanical Test | | | |
| Vibration test | Endurance test applying the vibration during transportation and using. | 10~22Hz→1.5mmp-p 22~500Hz→1.5G Total 0.5hrs | — |
| Shock test | Constructional and mechanical endurance test applying the shock during transportation. | 50G Half sign wave 11 msec 3 times of each direction | — |

***Supply voltage for logic system=5V. Supply voltage for LCD system =Operating voltage at 25°C