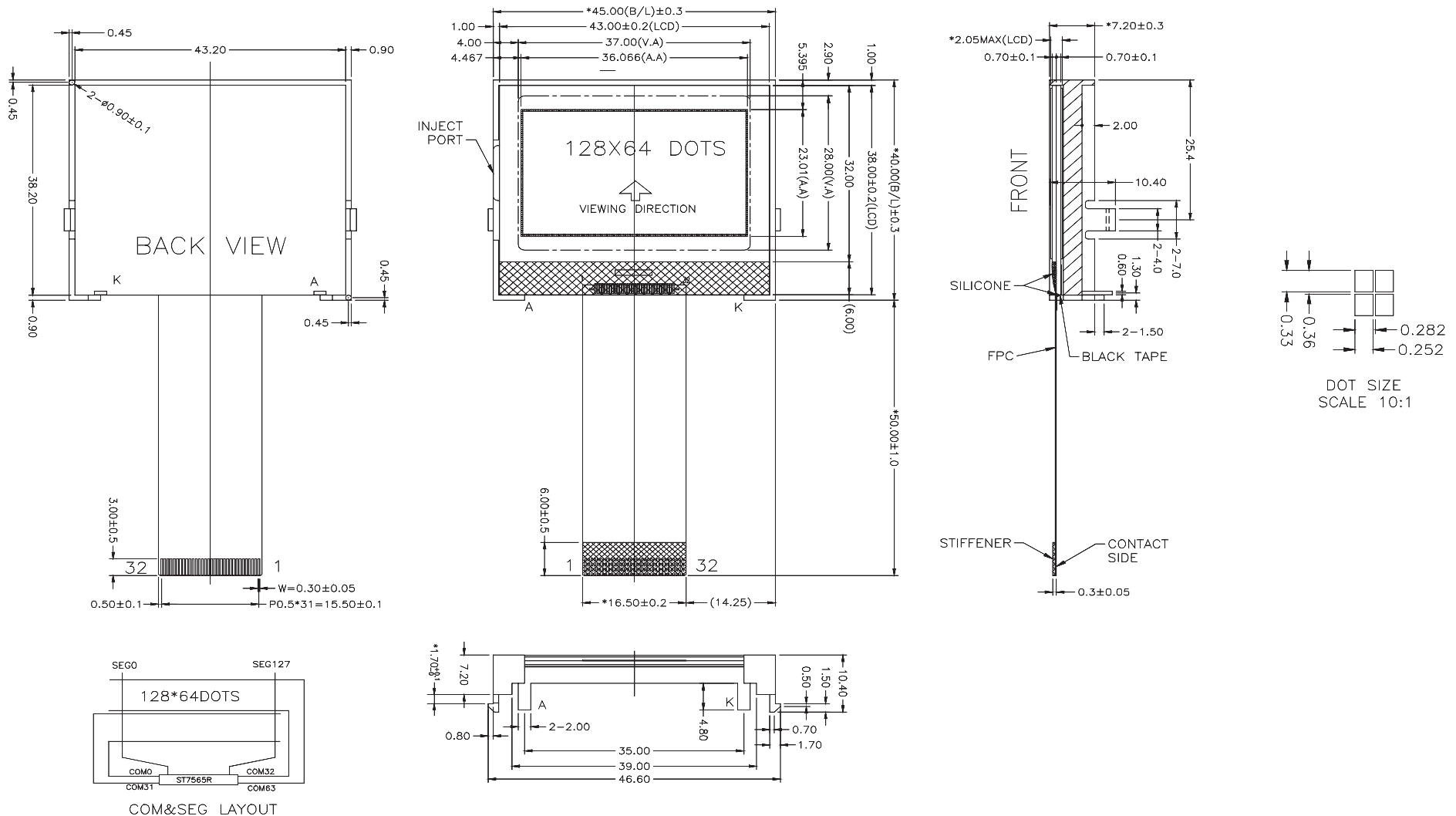


Unmarked tolerance ± 0.3



PIN	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
CONNECTOR	IRS	HPMB	PSB	C86	VR	V0	V1	V2	V3	V4	CAP4+	CAP2-	CAP2+	CAP1+	CAP1-	CAP3+
PIN	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
CONNECTOR	VOUT	VSS	VDD	D7	D6	D5	D4	D3	D2	D1	D0	RD (E)	WR (RW)	A0	/RES	/CS1